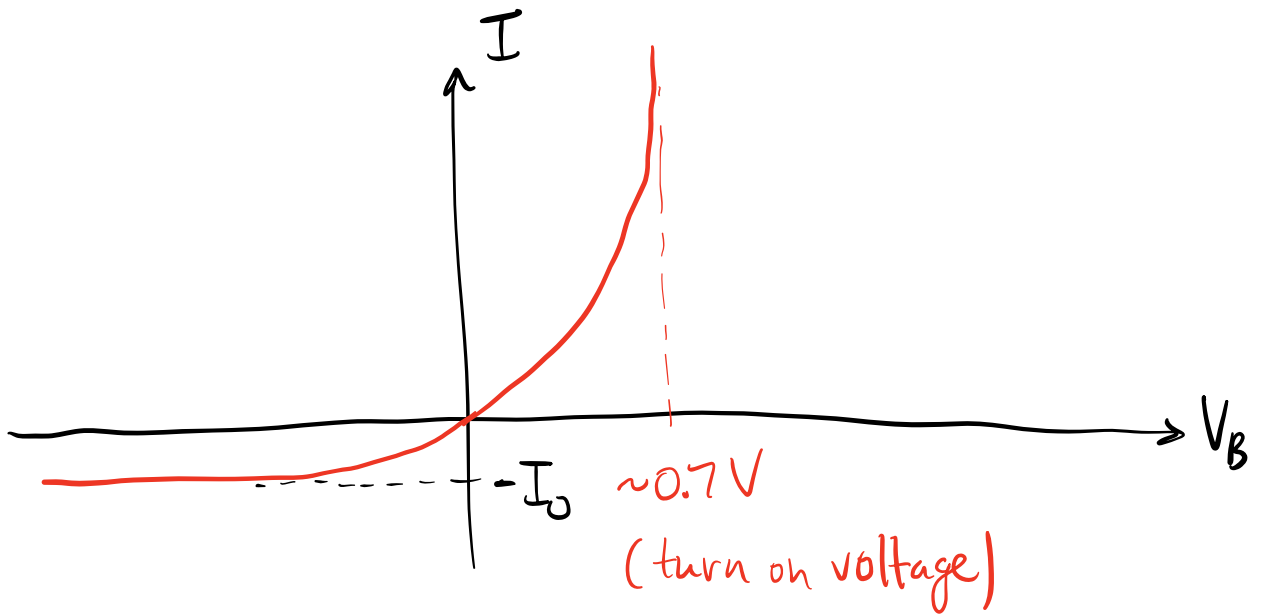


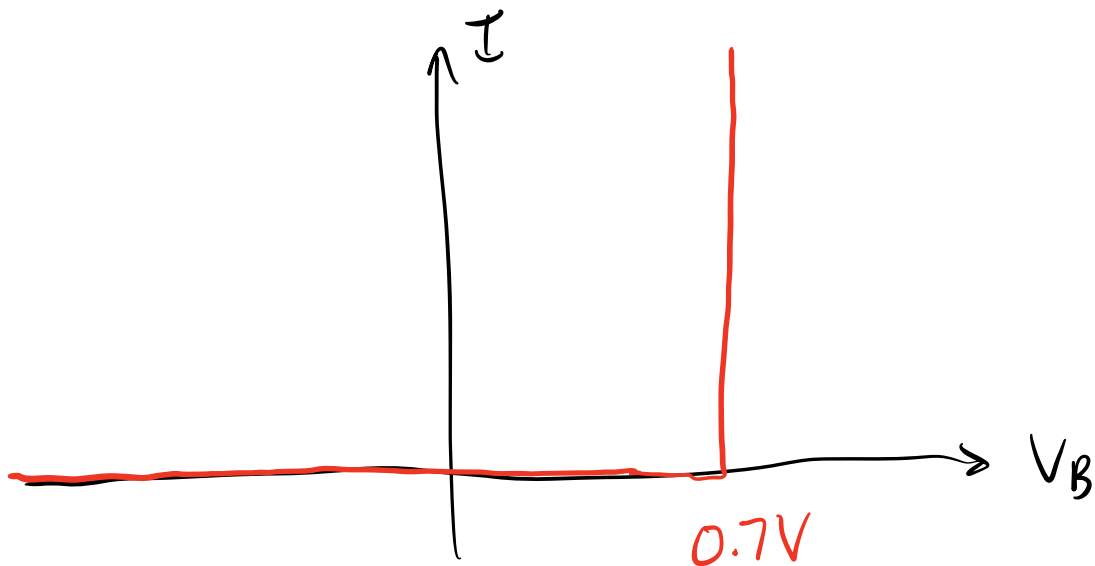
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Last Time: Diode current.

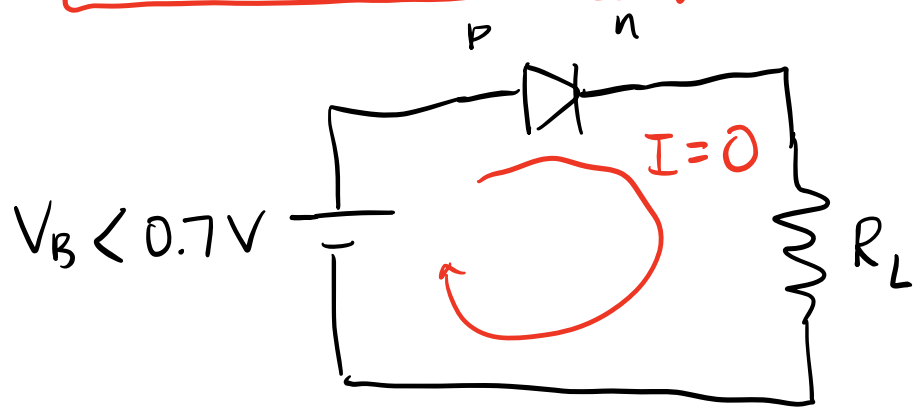
$$I = I_0 \left(e^{eV_B/k_B T} - 1 \right)$$



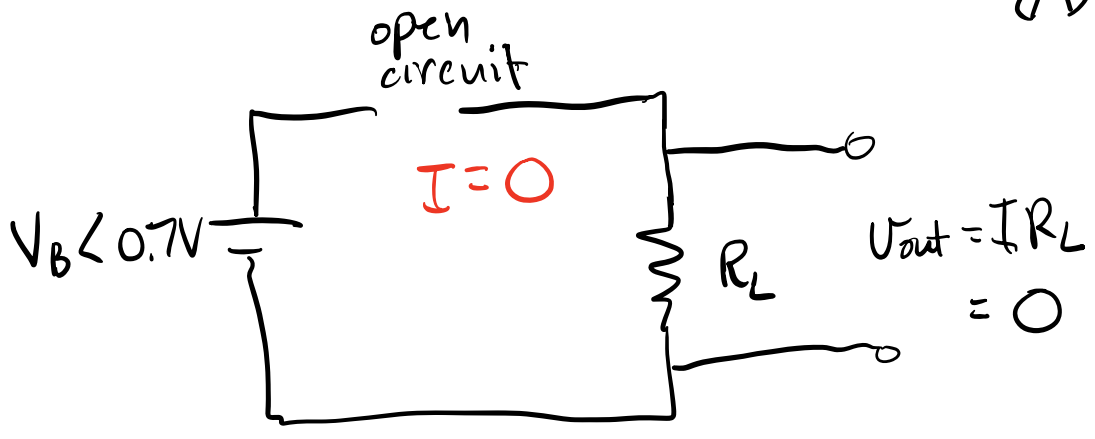
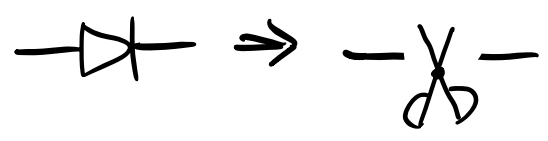
Approximate I - V characteristic as:



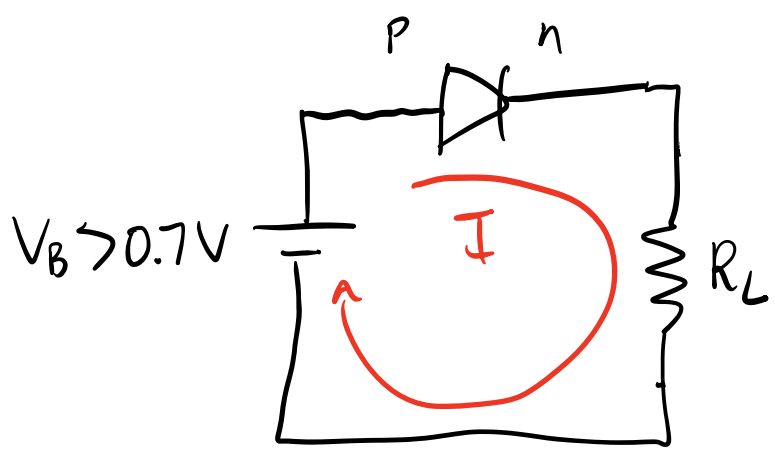
Reverse biased diode: diode "off", no current



Equiv. circuit:

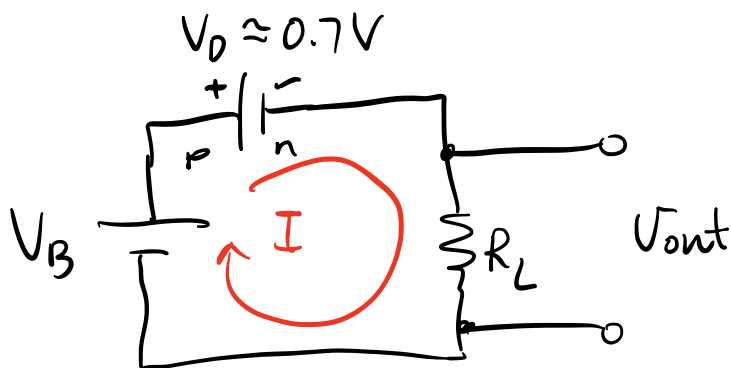
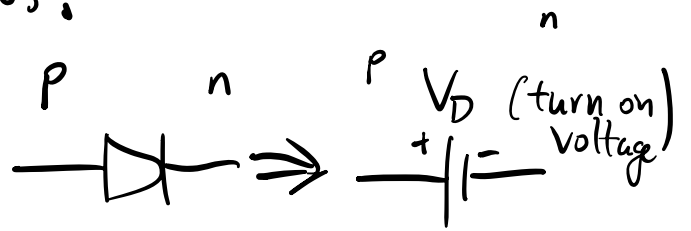


Forward Biased Diode: diode "on", $I \neq 0$.



Must overcome the 0.7V turn on voltage before current flows.

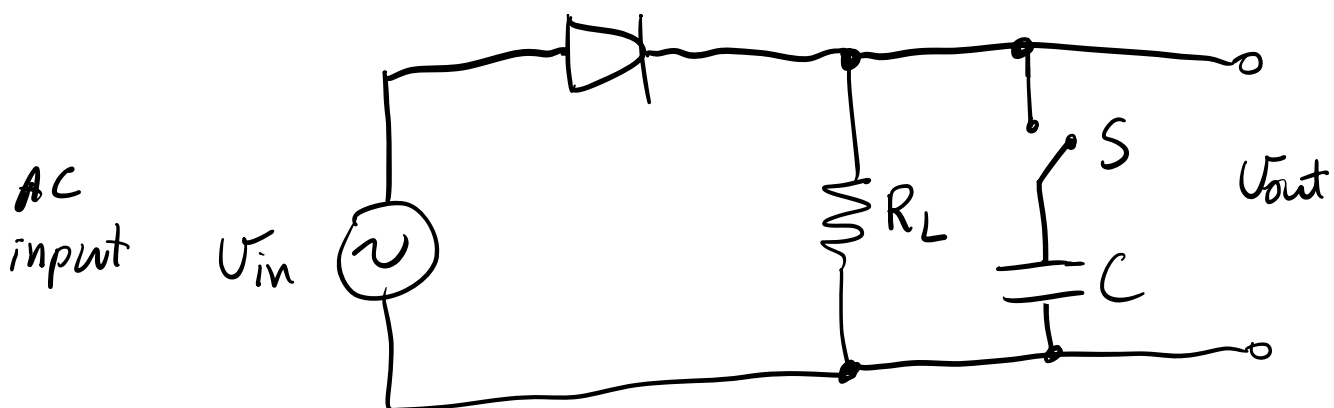
Equivalent circuit



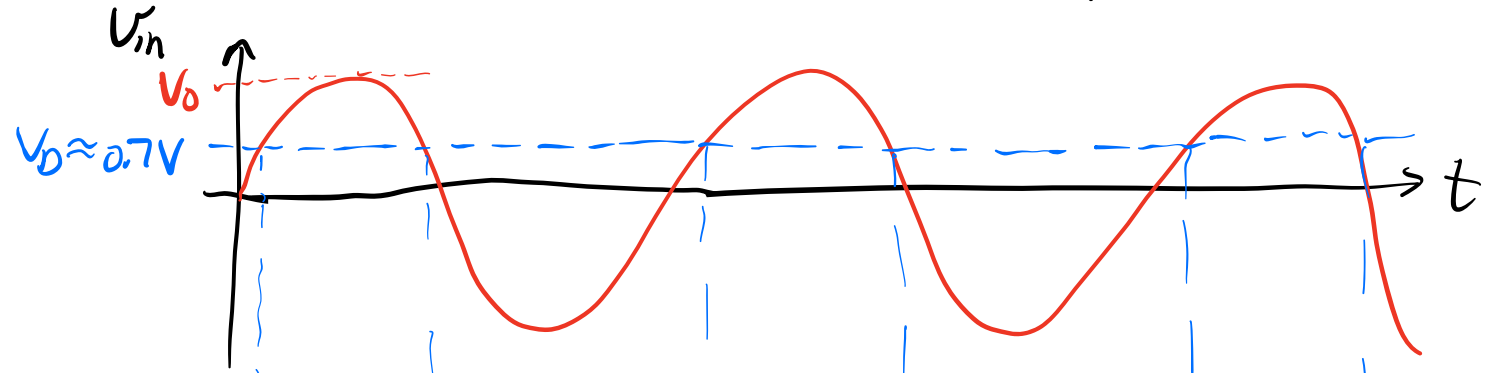
$$I = \frac{V_B - V_D}{R_L}$$

$$V_{out} = IR_L = V_B - V_D$$

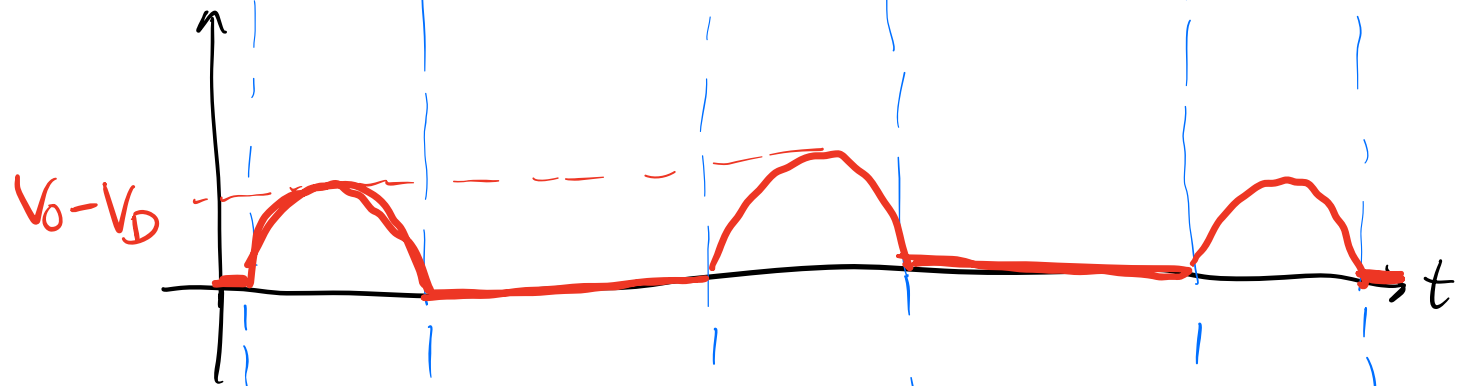
Rectifier Circuit (AC-to-DC conversion)



Plot V_{in} & V_{out} w/ switch open & the closed.



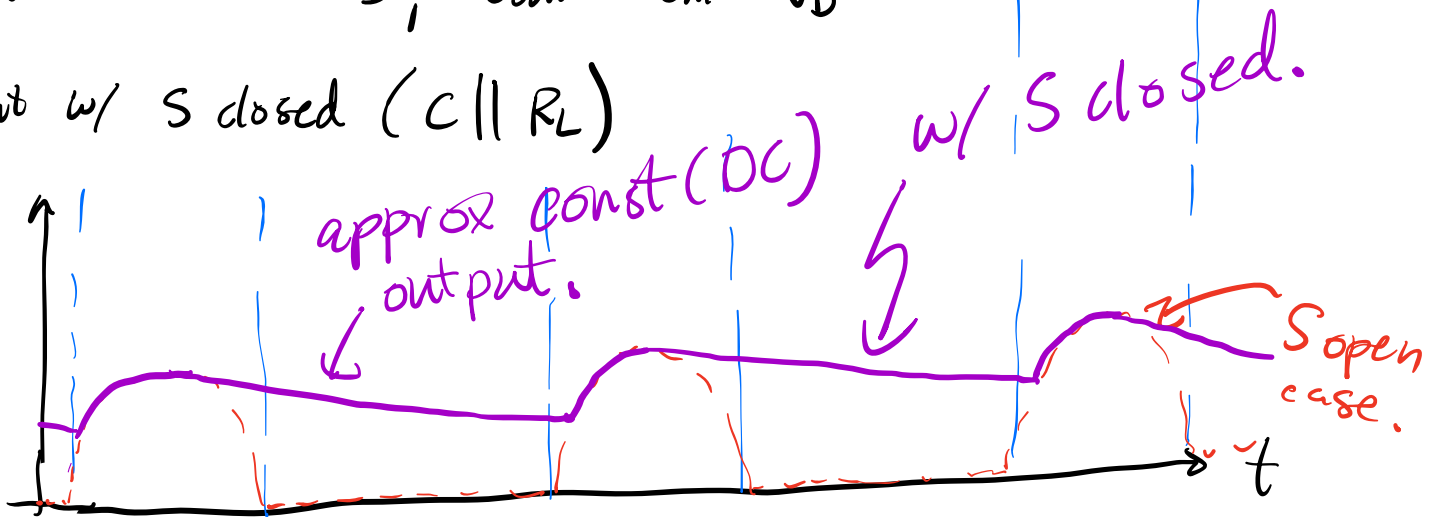
V_{out} w/ S open. (no cap.)



When $V_{in} < V_D \approx 0.7V$, no current & $V_{out} = 0$.

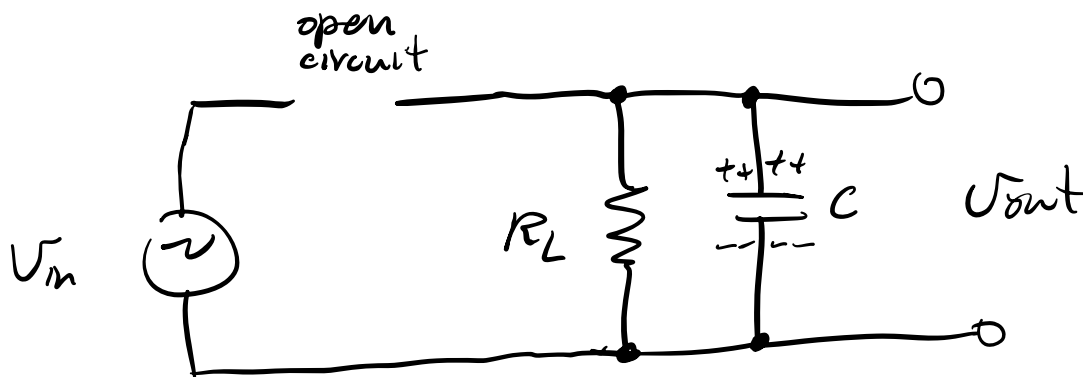
When $V_{in} > V_D$, $V_{out} = V_{in} - V_D$

V_{out} w/ S closed (C || R_L)



No change when diode is forward biased.
Still expect $V_{out} = V_{in} - V_D$ when cap is in place.

When $V_{in} < V_D$ \uparrow diode is reverse biased,
equiv. circuit becomes:



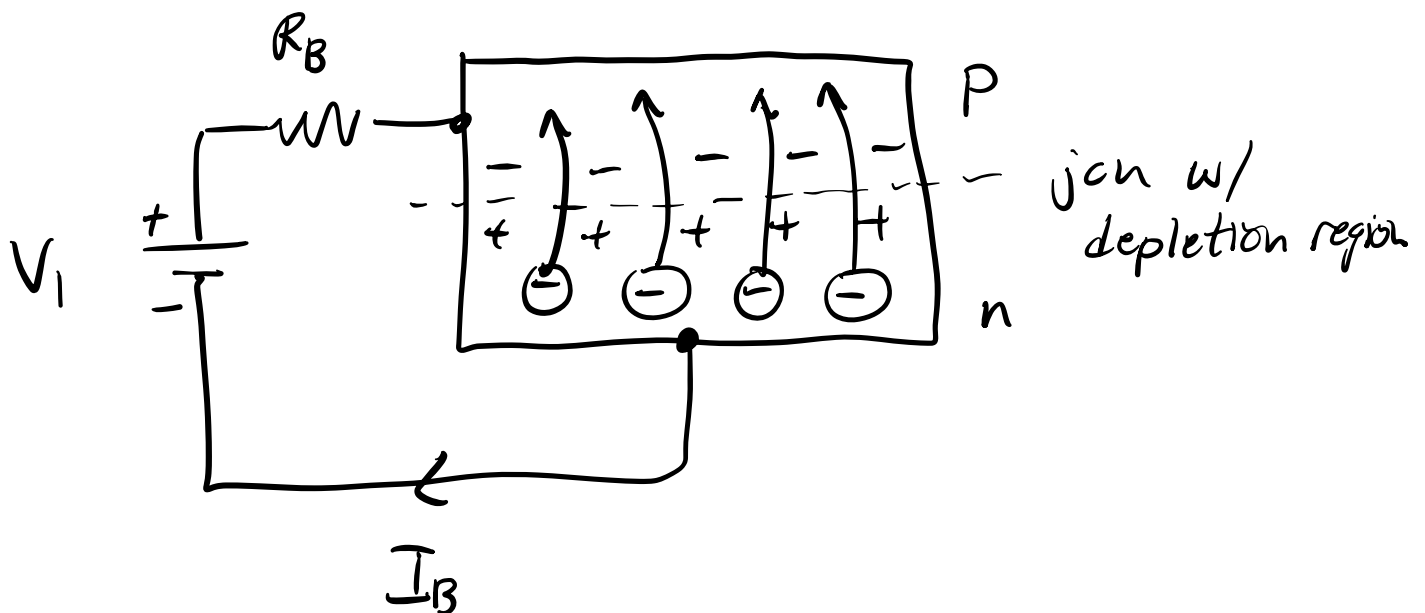
Cap charges when diode is forward biased.

Cap discharges through R_L when diode is reverse biased. Make the time const.

$R_L C$ long so discharge is slow.

Bipolar Junction Transistor (BJT)

Start w/ a forward biased diode

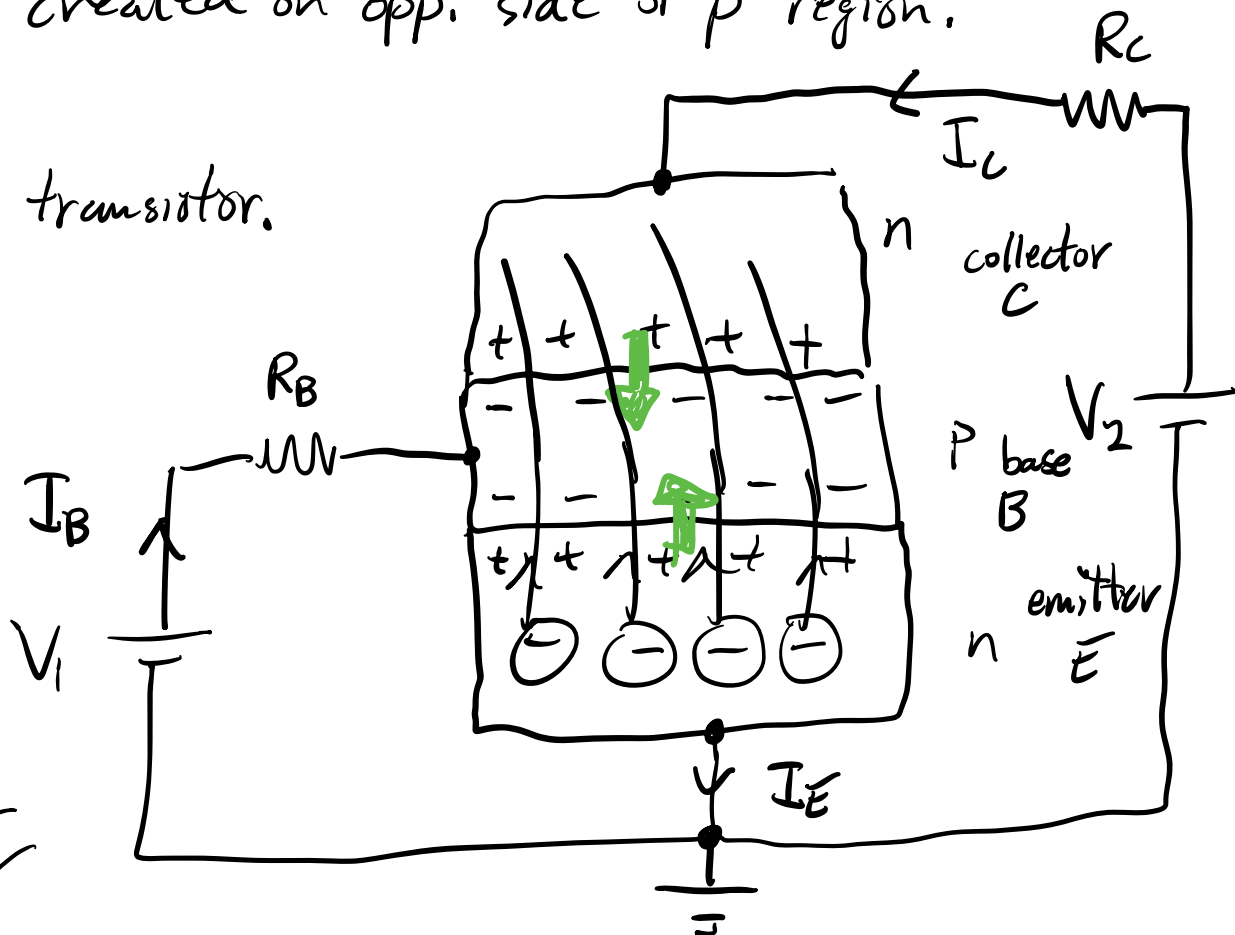


If $V_1 \geq V_D \approx 0.7V$, get large conduction across jcn.

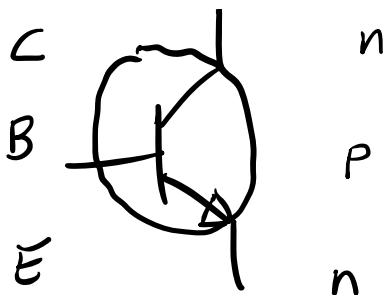
When making a transistor, n-region is much more heavily doped than p-region. \Rightarrow most of current is due to flow of e^- .

To complete the BJT, a second p-n junction is created on opp. side of p region.

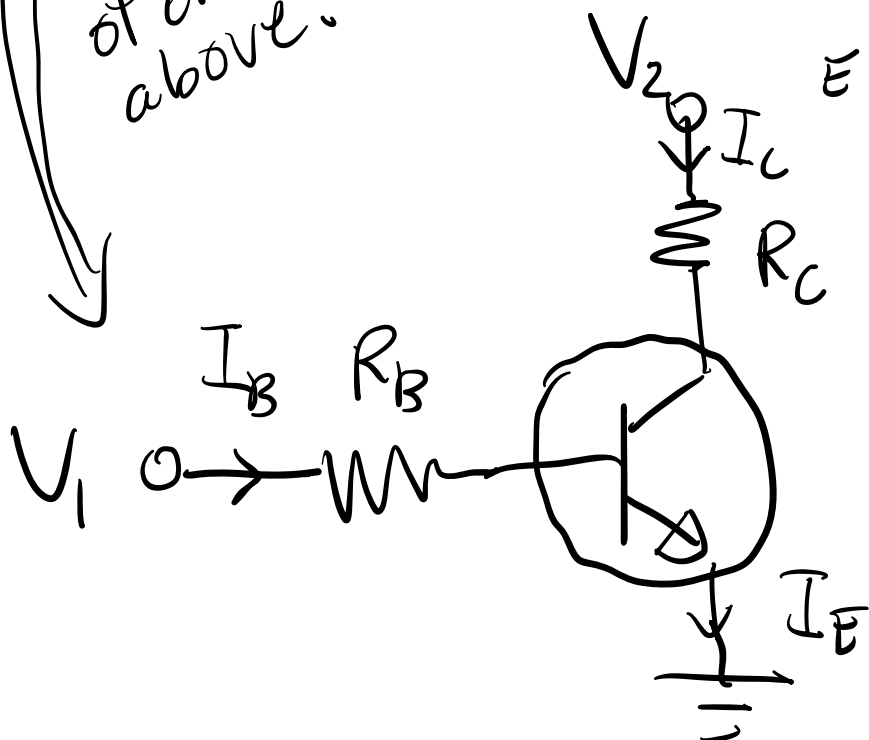
npn transistor.



Circuit symbol for npn transistor is



Schematic of circuit above.



Base of a BJT is made very thin.

\therefore most of e^- from emitter cross the base region & are swept into the collector

$\Rightarrow I_B$ is, therefore, always v. small.

By jcn rule: $I_B + I_C = I_E$

Since I_B is small, $I_C \approx I_E$

$$I_C = \alpha I_E \quad \text{typically } \alpha \approx 0.99$$

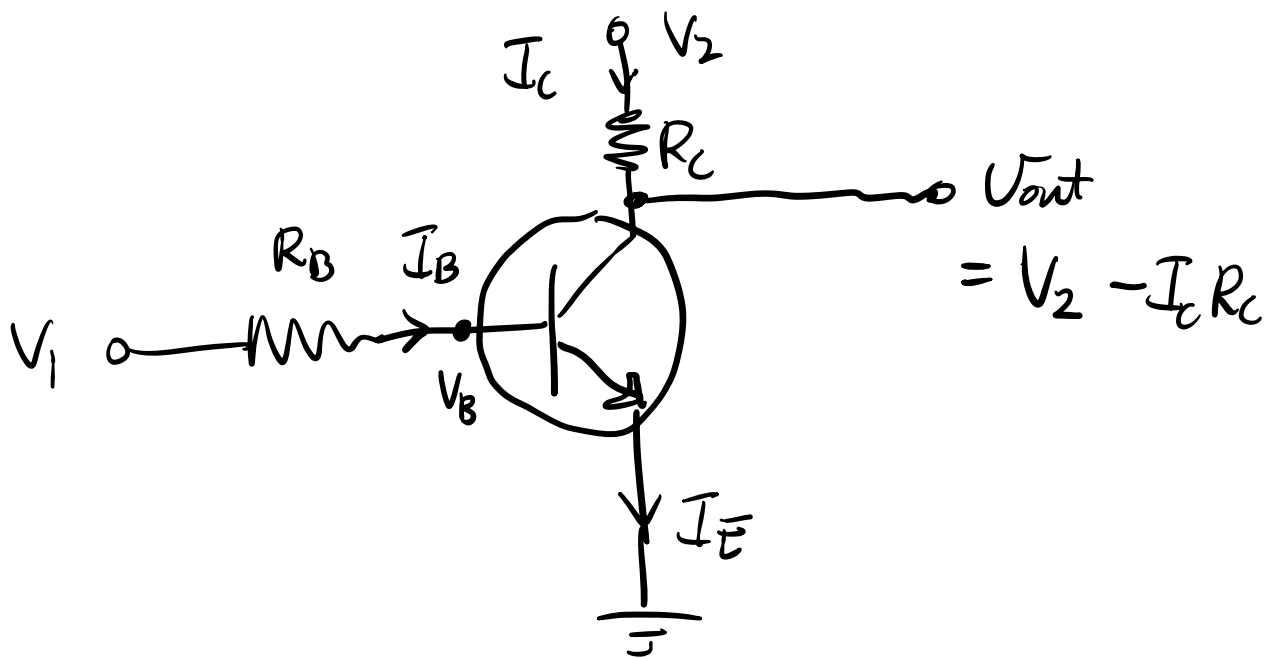
$$\begin{aligned} I_B &= I_E - I_C \\ &= I_E (1 - \alpha) \end{aligned}$$

$$\therefore \frac{I_C}{I_B} = \frac{\alpha I_E}{(1 - \alpha) I_E} = \frac{\alpha}{1 - \alpha} \approx 99$$

$$\therefore I_C \approx 99 I_B$$

large current gain.
 \rightarrow current amplifier.

Transistor as a Switch.



Since I_B is always small

$$V_1 - \cancel{I_B R_B}^0 = V_B \quad \therefore \quad V_1 \approx V_B$$

① $V_1 < 0.7V$, B-E junction is reverse biased
 $\therefore I_E = I_C = 0$.

$$V_{out} = V_2 - \cancel{I_C R_C}^0 \quad V_{out} = V_2$$

For example, if $V_1 = 0$ & $V_2 = 5V$

V_1	V_{out}
0	5V

 \Rightarrow

V_1	V_{out}
0	5V

② $V_1 > 0.7V$, B-E is forward biased
 $I_c \neq 0$.

$$V_{out} = V_2 - I_c R_c \approx 0$$

If $V_1 = 5V$ (HI), $V_{out} \approx 0V$ (LO)

Truth Table

V_1	V_{out}
LO	HI
HI	LO

Inverter.